

TMDS320C6678 Platform Development Kit

Release Notes

Applies to Product Release: 01.01.02.06
Publication Date: Nov 2, 2012



Texas Instruments, Incorporated
20450 Century Boulevard
Germantown, MD 20874 USA

Document License

This work is licensed under the Creative Commons Attribution-NoDerivs 3.0 Unported License. To view a copy of this license, visit <http://creativecommons.org/licenses/by-nd/3.0/> or send a letter to Creative Commons, 171 Second Street, Suite 300, San Francisco, California, 94105, USA.

Contributors to this document

Copyright (C) 2011 Texas Instruments Incorporated - <http://www.ti.com>

© Copyright 2011 Texas Instruments, Inc.
All Rights Reserved

Contents

1	Overview	1
2	Version Information for the Release	1
2.1	Software Version Information for 1.1.2.6	1
2.2	History	2
2.2.1	Software Version Information for 1.1.2.5	2
2.2.2	Software Version Information for 1.1.1.4	2
2.2.3	Software Version Information for 1.1.0.3	3
2.2.4	Software Version Information for 1.1.0.2	3
2.2.5	Software Version Information for 1.1.0.1	4
2.2.6	Software Version Information for 1.1.0.0	4
2.3	Hardware Support	5
2.4	Operating System Support	5
3	Known Issues	5
4	Limitations	5
5	Licensing	5
5.1	Directory Structure	6
5.2	Source Code Components	7
6	Required Compile Tools	7
7	PDK Package	7
7.1	Build Environment	7
8	Executing the PDK examples	7
9	Customer Documentation List	8

TMDS320C6678 PDK 1.01.02.06

1 Overview

This document provides information on the features, functions, delivery package, compile tools, licensing, and incident report (IR) resolutions incorporated for Platform Development Kit (PDK) release for **TMDS320C6678 R1.1.0**. For remaining sections of this document the `<pdk_version>` keyword will reflect the aforesaid version.

The package contains source, example and documentation deliverables for Chip Support Library (CSL), Low Level Drivers (LLDs) with transport and platform packages.

The transport has NDK's NIMU (Network Interface Management Unit) API for the C6678 device. Please refer to A.15 of spru524_pg.pdf located under “\ndk_2_##_##_##_\packages\ti\ndk\docs\stack” for more details.

The platform has the APIs implemented to initialize the platform, read/write to the devices like EEPROM, NAND and NOR. Please refer to the “platform_library_user_guide.html” located under “\pdk_C6678_1_1_0_##_\packages\ti\platform\docs” for more details.

The document includes a list of the known issues that are present at the time of this release.

2 Version Information for the Release

This release supports the following software and hardware versions.

2.1 Software Version Information for 1.1.2.6

Release	Version Information
CSL	Version 1.0.1.3
CPPI LLD	Version 1.0.2.2
QMSS LLD	Version 1.0.3.12
PA LLD	Version 1.2.2.1

SRIO LLD	Version 1.0.1.9
Hyperlink LLD	Version 1.0.1.5
TSIP LLD	Version 1.0.0.5
PCle LLD	Version 1.0.0.3
RM LLD	Version 1.0.0.10

2.2 History

2.2.1 Software Version Information for 1.1.2.5

Release	Version Information
CSL	Version 1.0.1.3
CPPI LLD	Version 1.0.2.2
QMSS LLD	Version 1.0.3.12
PA LLD	Version 1.2.2.1
SRIO LLD	Version 1.0.1.9
Hyperlink LLD	Version 1.0.1.5
TSIP LLD	Version 1.0.0.5
PCle LLD	Version 1.0.0.3
RM LLD	Version 1.0.0.10

2.2.2 Software Version Information for 1.1.1.4

Release	Version Information
CSL	Version 1.0.1.3
CPPI LLD	Version 1.0.2.2
QMSS LLD	Version 1.0.3.12
PA LLD	Version 1.2.2.1

SRIO LLD	Version 1.0.1.9
Hyperlink LLD	Version 1.0.1.5
TSIP LLD	Version 1.0.0.5
PCIe LLD	Version 1.0.0.3
RM LLD	Version 1.0.0.10

2.2.3 Software Version Information for 1.1.0.3

Release	Version Information
CSL	Version 1.0.1.3
CPPI LLD	Version 1.0.2.2
QMSS LLD	Version 1.0.3.12
PA LLD	Version 1.2.2.1
SRIO LLD	Version 1.0.1.9
Hyperlink LLD	Version 1.0.1.5
TSIP LLD	Version 1.0.0.5
PCIe LLD	Version 1.0.0.3
RM LLD	Version 1.0.0.7

2.2.4 Software Version Information for 1.1.0.2

Following table shows the software versions applicable to this release.

Versions Supported by this Release

Release	Version Information
CSL	Version 1.0.1.2
CPPI LLD	Version 1.0.2.2
QMSS LLD	Version 1.0.3.12
PA LLD	Version 1.2.2.1

SRIO LLD	Version 1.0.1.9
Hyperlink LLD	Version 1.0.1.5
TSIP LLD	Version 1.0.0.5
PCIe LLD	Version 1.0.0.3
RM LLD	Version 1.0.0.7

2.2.5 Software Version Information for 1.1.0.1

Following table shows the software versions applicable to this release.

Versions Supported by this Release

Release	Version Information
CSL	Version 1.0.1.1
CPPI LLD	Version 1.0.0.16
QMSS LLD	Version 1.0.0.17
PA LLD	Version 1.1.1.0
SRIO LLD	Version 1.0.1.1
Hyperlink LLD	Version 1.0.1.1
TSIP LLD	Version 1.0.0.4
PCIe LLD	Version 1.0.0.2

2.2.6 Software Version Information for 1.1.0.0

Following table shows the software versions applicable to this release.

Versions Supported by this Release

Release	Version Information
CSL	Version 1.0.1.1
CPPI LLD	Version 1.0.0.16
QMSS LLD	Version 1.0.0.17
PA LLD	Version 1.1.1.0

SRIO LLD	Version 1.0.1.1
Hyperlink LLD	Version 1.0.1.1
TSIP LLD	Version 1.0.0.4
PCIe LLD	Version 1.0.0.2

2.3 Hardware Support

This release is supported on the following hardware platform:

Platform Development Kit	Supported Devices	Supported EVMs
C6678	TMS320C6678, TMS320TCI6608,	TMDXEVM6678L

2.4 Operating System Support

This release includes driver examples which have dependencies on BIOS.

Refer individual LLD package release notes for additional dependent packages

Refer to platform library and NIMU driver projects for additional dependent packages.

3 Known Issues

Please refer to the individual components release notes for additional Known Issues and workarounds.

Table 3 Known Issue IRs for this Release

External Bug Query
SDOWP BIOS-MCSDK 2.0 (External)

4 Limitations

Please refer to the individual components release notes for this information

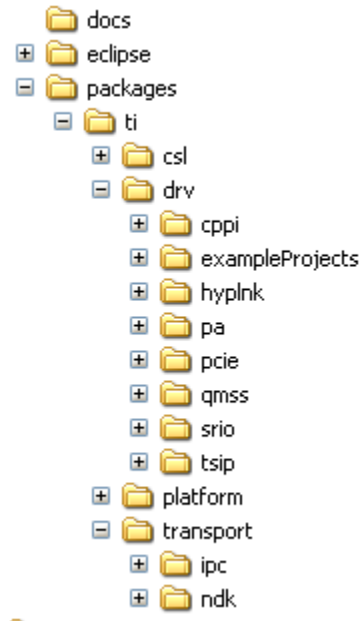
5 Licensing

Please refer to the software manifest which outlines the licensing status for all packages included in this release.

5.1 Directory Structure

Following is the directory structure after TMDS320C6678 PDK is installed. The default installation directory is

C:\ti\pdk_C6678_<pdv_version>.



The following table explains the contents of the PDK package:-

Directory Name	Description
docs	Release notes for PDK and individual components available in the package. Includes LLD User guide containing build guideline and steps to run examples included in the PDK
eclipse	Plug ins for the supporting documentation of package under CCSv5
packages	The packages directory is list of the components which are extracted out with pre-built libraries and are in a flat directory structure <i>without any version information</i> . This is the directory which is used for all builds etc. Additionally the components directory can contain additional tar versions not currently in-use in packages directory.

platform	Has the platform library APIs, which provide functions to access the flash/program pll functions.
transport/ndk	Has the Soc specific functions implemented to tie up to the NDK.
transport/ipc	Contains QMSS/SRIO transports for IPC package.

5.2 Source Code Components

- The PDK package is being provided as source components.

6 Required Compile Tools

Specific tools and patches must be used to compile and/or deploy Texas Instruments software. Texas Instruments cannot guarantee product performance when other tools are used.

7 PDK Package

7.1 Build Environment

Release package comes with a pre-built set of libraries for individual LLD's ,CSL modules, platform library and NIMU driver libraries.

Please refer to

http://processors.wiki.ti.com/index.php/BIOS_MCSDK_2.0_User_Guide#Low_Level_Drivers link for building the LLDs.

8 Executing the PDK examples

The PDK example projects are located under “\packages\ti\drv\exampleProjects” directory.

Please refer to the individual LLD directories for testing the example projects/more details.

9 Customer Documentation List

Each component exposes all the documentation in the corresponding components

LLD	Reference
CPPI	<ul style="list-style-type: none"> • \ti\drv\cp\example\sample\readme.txt
HYPLINK	<ul style="list-style-type: none"> • \ti\drv\hyplnk\example\memoryMappedExample\Readme.txt
PA	<ul style="list-style-type: none"> • \ti\drv\pa\example\emacExample\readme.txt • \ti\drv\pa\example\multicoreExample\readme.txt • \ti\drv\pa\example\simpleExample\readme.txt
PCIE	<ul style="list-style-type: none"> • \ti\drv\pcie\example\sample
QMSS	<ul style="list-style-type: none"> • \ti\drv\qmss\example\InfrastructureMode\Readme.txt • \ti\drv\qmss\example\InfrastructureModeMulticore\Readme.txt
SRIO	<ul style="list-style-type: none"> • \packages\ti\drv\srio\docs\ ReleaseNotes_SRIODriver.pdf
TSIP	<ul style="list-style-type: none"> • \ti\drv\tsip\example\sample\Readme.txt

“docs” directory. For the platform library please refer to “platform_library_user_guide.html” located under the “docs” directory of platform.